ADVANTECH

SIMB-A01
Intel® Q35 LGA775 socket for Intel® Core™ 2 Quad ATX Motherboard Startup Manual

Packing List

Before you begin installing your card, please make sure that the following items have been shipped:

- 1 x Intel Q35 ATX Main board
- 1 x CD ROM per carton, which contains the followings:
  - User's Manual in PDF file
  - Drivers
- 1 x SATA cable kit (SATA/Power)
- 1 x I/O Shield
- 1 x Startup Manual per carton

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

Note1:

Acrobat Reader is required to view any PDF file. Acrobat Reader can be downloaded at: www. adobe.com/Prodindex/acrobat/readstep.html (Acrobat is a trademark of Adobe).

For more information on this and other Advantech products, please visit our website at:

http://www.advantech.com

http://www.advantech.com/eplatform

For technical support and service, please visit our support website at:

http://www.advantech.com/support

This manual is for the SIMB-A01 series Rev. A1.

Part No. 20060A0110   1st Edition
Printed in China   July 2011

Specifications

System

- **CPU:** Intel® Q35 LGA775 socket for Intel® Core™ 2 Quad / Core™2 Duo / processors compatible with Intel® 05B/05A/06 processors support Intel® next generation 45nm multi-core CPU
- **FSB:** 1333 / 1066 / 800 MHz
- **BIOS:** AMI 32 Mb SPI BIOS
- **System Chipset:** Intel Q35 GMCH/ICH9R
- **I/O Chipset:** Winbond W83627DHG-A
- **Memory:** Four 240-pin DIMM sockets support up to 8 GB Dual Channel DDR2 800/667 SDRAM
- **Watchdog Timer:** Reset: 1 sec.~255 min. and 1 sec. or 1 min./step
- **H/W Status Monitor:** Monitoring temperatures, voltages, and cooling fan status. Auto throttling control when CPU overheats
- **Expansion Slots:** 1 x PCI Express x16, 1 x PCI Express x1, 5 x PCI (PCI Rev. 2.3 compliant)
- **DIO:** 16 bit (8-in/8-out by PCA9555)
- **S3 / S4:** Yes
- **TPM:** TPM1.2 (Infineon® TPM chip 9635 TT 1.2 on board)
- **Wake up on LAN or Ring:** LAN (PME / RPL)
- **Smart Fan Control:** Yes , support 3 modes (Silent/Optimal/Performance)

Display

- **Chipset:** Intel Q35 GMCH integrated Graphics Media Accelerator 3100
- **Display Memory:** Intel DVMT 4.0 supports up to 384 MB video memory
- **Max. Resolution:** 2048 x 1536 bpp(@ 75Hz)
- **VGA:** Yes , on board GMA 3100
- **LVDS / DVI / HDMI:** Through ADD2 LVDS Card
- **Secondary VGA:** Yes , through ADD2 card

Audio

- **Audio Codec:** Realtek® ALC888, 7.1 W/O multiple streaming or 5.1+2 with multiple streaming HD audio
- **Audio Interface:** Mic in, Line in, Line out
- **Audio Amplifier (W):** Dual 6W Amp.(TPA3005D2 , Optional)

Ethernet

- **LAN1:** RTL8111C Gigabit LAN
- **LAN2:** RTL8111C Gigabit LAN

Connectors and Jumpers

Connectors		
Label	**Function**	**Note**
AAFP	Front Panel Audio Connector	
CHA_FAN1	Chassis Fan Connector	
CPU_FAN	CPU Fan Connector	
F_PANEL	System Panel Connector	
SPDIF_OUT	Digital Audio connector	

Connectors and Jumpers

Jumpers

Label	Function	Note
CLRTC	Clear CMOS	Norrnal* / Clear CMOS
CHASSIS	Chassis Intrusion Connector	GND / Chassis Signal / +5VSB
JCOMPWR 1,2,3,4	COM1,2,3,4 RI/+5V/+12V Select	RI* / +5V / +12V
JCOMPWR 5,6,7,8	COM4 RS-232/422/485 Select	JCOMPWR5 / JCOMPWR6, 7 — RS-232* / RS-422 / RS-485

Disclaimer and Notice

The manufacturer reserves the right to make changes, without notice, to any product, including circuits and/or software described or contained in this manual in order to improve design and/or performance. The manufacturer assumes no responsibility or liability for the use of the described product(s), conveys no license or title under any patent, copyright, or masks work rights to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified. For the detail product information, please refer to user's manual.

Safety Declaration

This device complies with the requirements in Part 15 of the FCC rules. Operation is subject to the following two conditions:

1. This device may not cause harmful interference.

2. This device must accept any interference received, including interference that may cause undesired operation.

Board Diagram