



# Intel® Xeon® Silver 4216 Processor


22M Cache, 2.10 GHz

Spec Code

SRFBB

Ordering Code

CD8069504213901

<b>Essentials</b>	
Product Collection	2nd Generation Intel® Xeon® Scalable Processors
Code Name	Products formerly Cascade Lake
Vertical Segment	Server
Processor Number	4216
Status	Launched
Launch Date	Q2'19
Lithography	14 nm
Use Conditions	Server/Enterprise
Recommended Customer Price	\$1002.00 - \$1012.00
<b>Performance</b>	
# of Cores	16
# of Threads	32
Processor Base Frequency	2.10 GHz
Max Turbo Frequency	3.20 GHz
Cache	22 MB
# of UPI Links	2
TDP	100 W
<b>Supplemental Information</b>	
Embedded Options Available	Yes
<b>Memory Specifications</b>	
Max Memory Size (dependent on memory type)	1 TB
Memory Types	DDR4-2400
Maximum Memory Speed	2400 MHz
Max # of Memory Channels	6
ECC Memory Supported 	Yes
Intel® Optane™ DC Persistent Memory Supported	No
<b>Expansion Options</b>	
Scalability	2S
PCI Express Revision	3.0

Max # of PCI Express Lanes	48
<b>Package Specifications</b>	
Sockets Supported	FCLGA3647
TCASE	79°C
Package Size	76.0mm x 56.5mm
<b>Advanced Technologies</b>	
Intel® Deep Learning Boost (Intel® DL Boost)	Yes
Intel® Speed Select technology - Performance Profile	No
Intel® Speed Select technology - Base Frequency	No
Intel® Resource Director Technology (Intel® RDT)	Yes
Intel® Speed Shift Technology	Yes
Intel® Turbo Boost Max Technology 3.0 ‡	No
Intel® Turbo Boost Technology ‡	2.0
Intel® vPro™ Platform Eligibility ‡	Yes
Intel® Hyper-Threading Technology ‡	Yes
Intel® Virtualization Technology (VT-x) ‡	Yes
Intel® Virtualization Technology for Directed I/O (VT-d) ‡	Yes
Intel® VT-x with Extended Page Tables (EPT) ‡	Yes
Intel® TSX-NI	Yes
Intel® 64 ‡	Yes
Instruction Set Extensions	Intel® AVX-512
# of AVX-512 FMA Units	1
Enhanced Intel SpeedStep® Technology	Yes
Intel® Volume Management Device (VMD)	Yes
<b>Security &amp; Reliability</b>	
Intel® AES New Instructions	Yes
Intel® Trusted Execution Technology ‡	Yes
Execute Disable Bit ‡	Yes
Intel® Run Sure Technology	No
Mode-based Execute Control (MBE)	Yes