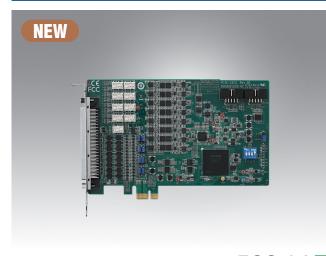
PCIE-1812

250 kS/s, 16-Bit, 8-Ch, Simultaneous Sampling Multifunction PCI Express DAQ Card



Features

- 8 differential simultaneous sampling analog inputs, up to 250 kS/s, 16-bit resolution
- 2 analog outputs, up to 3 MS/s, 16-bit resolution
- 2 analog triggers and 2 digital triggers for analog I/O
- 32 programmable DI/Os with interrupt functions
- Four 32-bit programmable counters/ timers/ encoders
- Board ID switch

FCC C E

Introduction

PCIE-1812 is a simultaneous-sampling multifunction DAQ card designed to meet a wide range of application requirements. PCIE-1812 supports simultaneous sampling of 8 analog input channels with differential input configuration for maximum noise elimination. In addition to providing 2-ch, 16-bit analog outputs with waveform generation capabilities, PCIE-1812 supports simultaneous waveform generation and analog input functions.

Specifications

Analog Input

- Channels .
- Mode
- Resolution
- Sample Rate
- Input Impedance Sampling Mode
- Input Range
- Accuracy

Range	±10 V	±5 V	±2.5 V	±1.25 V	±0.625 V
Accuracy	±0.01%	±0.01%	±0.01%	±0.01%	±0.01%
Range		0 ~ 10 V	0 ~ 5 V	0 ~ 2.5 V	0 ~ 1.25 V
Accuracy		±0.01%	±0.01%	±0.01%	±0.01%

Software programmable

Differential input

250 kS/s max.

16 bits

1GΩ

2 16 bits

Analog Output

- Channels
- Resolution

•	Output Rate	3 M max.
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 Output Range Softwar 		rogrammable
Internal Deference	Unipolar	0 ~ 5 V, 0 ~ 10 V
Internal Reference	Bipolar	-5 V ~ 5 V, -10 V ~ 10 V
External Reference		0 ~ +x V @ −x V (−10 ≤ x ≤ 10)

Static update, Buffered (Waveform generation)

Yes. Hysteresis range is configurable

Rising edge or falling edge, selected by software

20 V/µs

0.01%

16 bits

-10 ~ 10 V

2

±20 mA max

- Slew Rate
- Driving Capability
- **Operation Mode**
- Accuracy

Analog Trigger

Channels

- Input Range
- Hysteresis
- Trigger Edge

Digital Trigger

	0 00	
•	Channels	2
•	Input Voltage	Logic 0: 1.5 V max.
		Logic 1: 3.5V min.
	Trigger Edge	Rising edge or falling edge, selected by software

Digital I/O

- Channels
- Output Voltage

32 (shared) Logic 0: 1.5 V max. Logic 1: 3.5 V min. Low 0.5 V max. @ 20 mA sink High 4.5 V min. @ 20 mA source/5.2 V max.

Counter/ Timer/ Encoder

- Channels -Resolution
 - Compatibility
 - 5 V/TTL Max. Input Frequency 10 MHz
- Counter/Timer Functions Frequency measurement, pulse width
- measurement, pulse output, PWM output Encoder Functions Quadrature (X1, X2, X4), dual pulse (CW/CCW), signed pulse (OUT/DIR)

PCI Express x1

multifunction card

4

32 bits

General

- . Form Factor
- I/O Connector .
- Dimensions (L x W) .
 - **Operating Temperature** 0 ~ 60 °C (32 ~ 140 °F) (refer to IEC 68-2-1, 2)
 - Storage Temperature
- **Storage Humidity**
- Board ID
- -40 ~ 70 °C (-40 ~ 158 °F) 5 ~ 95% RH non-condensing (refer to IEC 68-2-3) TM switch

100-pin SCSI female ribbon-type connector

175 x 100 x 18 mm³ (6.9 x 3.9 x 0.7 in.³)

Ordering Information

PCIE-1812-B

Accessories

- PCL-101100R-1E PCL-101100R-2E
- ADAM-39100-BE
- PCLD-8813-AE
- 1700030423-01

100-pin SCSI shielded cable, female to male, 1 m 100-pin SCSI shielded cable, female to male, 2 m 100-pin DIN rail SCSI wiring board

250 kS/s, 16-bit, 8-ch simultaneous sampling

- 6Advanced Signal Conditioning Board for
- PCIE-1812/PCIE-1813 Low-Pass Active Filter Boar
- PCLD-8811-AE
- 10-pin flat cable for MDSI synchronization, 10 cm

- Software and external clock
- Input Voltage