PCIE-1841 PCIE-1841L

16/8-ch, 18-bit, 1MS/s/ch Simultaneous **Analog Input PCI Express DAQ Card**



Specifications

Analog Input

- Channels Analog-to-digital converter 18 bits (ADC) resolution
- Input range
- Maximum input voltage ±20 V

•	Input common-mode	volta
	±20 V range	±1
	±12.5 V range	±6
	±10 V range	±5
	±5 V range	±2
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- Over-voltage protection ±30 V
- Input coupling Input impedance Voltage input Current input
- Analog low-pass filter -3 dB bandwidth
- Acquisition type
- **Buffered Acquisition**
- Enabled channel combination by software Sample rate software configurable
- On-board FIFO Size

Absolute accuracy

- Voltage input
- Operating temperature within ±5°C of last Auto-calibration temperature Over full operating temperature range Current input Operating temperature within ±5°C of last
- Auto-calibration temperature Over full operating temperature range

DC Performance⁽²⁾

	Idle channel noise	275 µVRMS
•	ENOB	17.14 bits

AC Performance⁽²⁾

-	SNR	88.36 dB
•	THD	-103.09 dB
•	THD+N	-85.29 dB
•	SFDR	101.95 dB
•	Dynamic Range	94.71 dB
	Crosstalk	-104.13 dB

Features

- 16/8-ch simultaneous sampling up to 1 MS/s
- 18-bit resolution

Trigger

- Software selectable low-pass filter
- Wide input range up to 40 Vpp (±20 V range)
- Support both voltage and current measurement
- Multiple selectable trigger modes
- Number of triggers Trigger action Start, delay to start, stop, or delay to stop Trigger delay range 0 ~ 16,777,215 samples 16/8 differential Sample number 0~16,777,215 samples Analog Trigger⁽¹⁾ ±20 V, ±12.5 V, ±10 V, ±5 V, or Channel ±20 mA, software configurable per channel 2 (start and stop) Source One of the analog input channels, software configurable ige range Threshold level Full scale of analog input range, software configurable 0 V 1/256 of analog input range, software configurable Hysteresis 6.25 V Polarity Rising edge or falling edge, software configurable 5 V 2.5 V Digital Trigger⁽¹⁾ Source 2 external pins DC Input logic level Logic high 2.0 V min. Logic low 0.8 V max. 1 MΩ Working voltage -0.25 V ~ 5.25 V 500Ω Rising edge or falling edge, software configurable Polarity Input protection voltage -0.5 V ~ 6.5V 22.5 kHz or 250 kHz, software configurable per channel Instant or buffered, software configurable **Mechanical Connector Type** DB-62 connector Dimension 175 x 100mm (6.9" x 3.93") Each channel can be enabled/disabled independently • Weiaht 0.12 ka 1 MHz max., for all channels, simultaneous sampling. Environment 8192 Samples Operating temperature 0 °C to 60 °C (-4 °F to 140 °F) Storage temperature -40 °C to 70 °C (-40 °F to 158 °F) Operating humidity 10% to 90% RH, non-condensing Storage humidity 5% to 95% RH, non-condensing ±0.01% of full-scale range max. Certification ±0.05% of full-scale range max. EMC CE, FCC **Ordering Information** ±0.1% of full-scale range max. ±0.5% of full-scale range max. PCIE-1841-A 16-ch, 18-bit, 1MS/s/ch, simultaneous sampling card PCIE-1841L-A 8-ch, 18-bit, 1MS/s/ch, simultaneous sampling card S Accessories

 - - 10 pin Flat Cable for MDSI synchronization, 10cm

(1) Total 2 triggers available, trigger mode and type selectable between analog/digital triggers (2) For detailed information, please refer to user manual

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- ADAM-3962-AE
- PCL-10162-1E PCL-10162-3E
- 1700030423-01
- DB-62 Wiring Terminal, DIN-rail Mount DB-62 Shielded Cable, 1m
- DB-62 Shielded Cable, 3m